



VIVEKANANDHA COLLEGE OF ENGINEERING FOR WOMEN
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Department of Electronics and Communication Engineering

Staff Publications- 2017-2018

S.NO	NAME OF THE FACULTY	NAME OF THE JOURNAL	TITLE	VOLUME & YEAR
1.	Dr.D.Sasikala	International Journal of Advance Computing, Communication and Technologies	High Performance and Low Power 2D DWT Architecture for Identification of Carotid Asymptomatic Plaque using FPGA	Vol.-4 Issue-3 Sep-2017
2.	Dr.V.M.Senthil Kumar	International Journal of Advance Computing, Communication and Technologies	Design a FINFET Based Multiplier for Power and Efficient Area	Vol.-4 Issue-2 Sep-2017
3.	Dr.V.M.Senthil Kumar	International Journal of Advance Computing, Communication and Technologies	Low Power Comparator Design using Domino logic and FINFET technology	Vol.-4 Issue-2 Sep-2017
4.	Mr. V. Karthikeyan	International Journal of Advance Computing, Communication and Technologies	ECG Adaptive Sampling Signals for Classification of Heart Beat and R-Wave Detection using VLSI Implementation	Vol.-4 Issue-2 Sep-2017
5.	Ms.M.Udhyavani	International Journal of Advance Computing, Communication and Technologies	Power Efficient Voltage Level Shifter and High Speed for Dual Supply	Vol.-4 Issue-2 Sep-2017
6.	Ms.M.Hemalatha	International Journal of Advance Computing, Communication and Technologies	Design of High SPEED Approximate 32 Bit Adder for Computation Utilizing Low Power and Effective Area	Vol.-4 Issue-2 Sep-2017
7.	Mr.M.Gunasekaran	International Journal of Advance Computing, Communication and Technologies	Combinational and Sequential Circuits Based Sub Threshold Adiabatic Logic	Vol.-4 Issue-2 Sep-2017

8.	Ms. S.Kokila	International Journal of Advance Computing, Communication and Technologies	Real Time Video Stabilization using FPGA	Vol.-4 Issue-2 Sep-2017
9.	Mr. M.Balasubramani	International Journal of Advance Computing, Communication and Technologies	Context-Based Adaptive Variable-Length Coding for H.264 using FPGA	Vol.-4 Issue-2 Sep-2017
10.	Mr.P.Nagarajan	International Journal of Advance Computing, Communication and Technologies	An Effective Low Power High Gain Op-Amp Design for Bio-Medical Application	Vol.-4 Issue-2 Sep-2017
11.	Mr. M.Thirupathi	International Journal of Advance Computing, Communication and Technologies	Low Power CMOS Comparator with 0.25nm for Sigma-Delta ADC	Vol.-4 Issue-2 Sep-2017
12.	Mr. S.Gnanamurugan	International Journal of Advance Computing, Communication and Technologies	High Speed Performance and Energy Efficient 256-bit CMOS Priority Encoder and Decoder	Vol.-4 Issue-2 Sep-2017
13.	Mr. T.Jayasimha	International Journal of Advance Computing, Communication and Technologies	An Effective ECG Detection using Low Power OTA-C Low Pass Filter	Vol.-4 Issue-2 Sep-2017
14.	Dr. D.Sasikala	Indo-Iranian Journal of Scientific Research	Design Of High Performance And Efficient Architecture For Lifting Based 2d Discrete Wavelet Transform In VLSI	Volume-2 Issue-1 January-March 2018
15.	Ms.M.Hemalatha	Indo-Iranian Journal of Scientific Research	Implementation Of High Speed Approximate Adder For Effective Area And Low Power	Volume-1 Issue-1 January-March 2018
16.	Mrs.S.Kokila, Ms.M.Karthika	Indo-Iranian Journal of Scientific Research	Real Time Of Video Stabilization Using Field-Programmable Gate Array (FPGA)	Volume-1 Issue-1 January-March 2018
17.	Mrs.M.Udhayavani	Indo-Iranian Journal of Scientific Research	A High Speed And Power Efficient Voltage Level Shifter For Dual Supply Applications	Volume-1 Issue-1 January-March 2018

18.	G.Ravikumar	Indo-Iranian Journal of Scientific Research	Design Of A Power Efficient Comparator By Using Domino Logic	Volume-1 Issue-1 January-March 2018
19.	Mr.M.Thiruppathi	Indo-Iranian Journal of Scientific Research	Design Of Sigma-Delta Analog-To-Digital Converter By Using Low Power 0.25 μm CMOS Comparator	Volume-1 Issue-1 January-March 2018
20.	V.Karthikeyan	Indo-Iranian Journal of Scientific Research	VLSI Implementation For R-Wave Detection And Heartbeat Classification Of ECG Adaptive Sampling Signals	Volume-1 Issue-1 January-March 2018
21.	M.Gunasekaran	Indo-Iranian Journal Of Scientific Research	Implementation Of Sleep Transistor Based SRAM Using Resonant Supply Boosting For Low Power Application	Volume-1 Issue-1 January-March 2018
22.	M.Balasubraman	Indo-Iranian Journal Of Scientific Research	A Novel Dual Stage Pipeline Method Of H.264 CAVLC Video Coding Using FPGA	Volume-1 Issue-1 January-March 2018
23.	S.Gnana murgan	Indo-Iranian Journal of Scientific Research	High Speed Performance And Energy Efficient 256-BIT CMOS Priority Encoder	Volume-1 Issue-1 January-March 2018
24.	Mr.T.Jayasimha	Indo-Iranian Journal of Scientific Research	Low Pass Filter Using ECG Detection For OTA-C	Volume-1 Issue-1 January-March 2018
25.	Dr.V.M.Senthil Kumar	Indo-Iranian Journal of Scientific Research	Design Of Approximate DADDA Multiplier For Low Power And Efficient Area	Volume-1 Issue-1 January-March 2018